

Fig 1

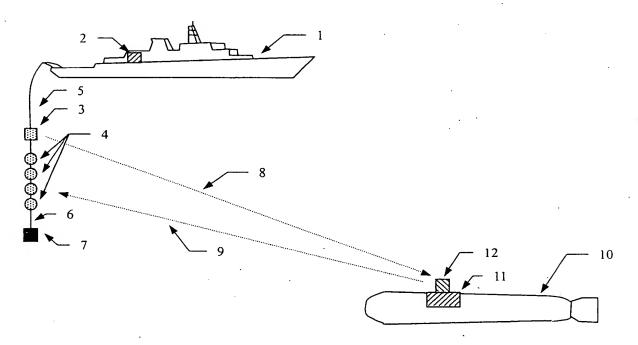
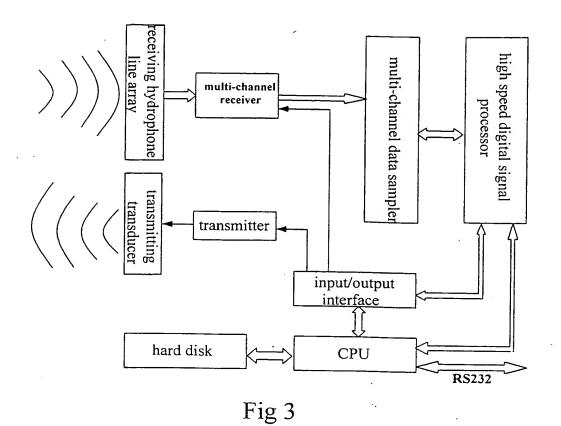
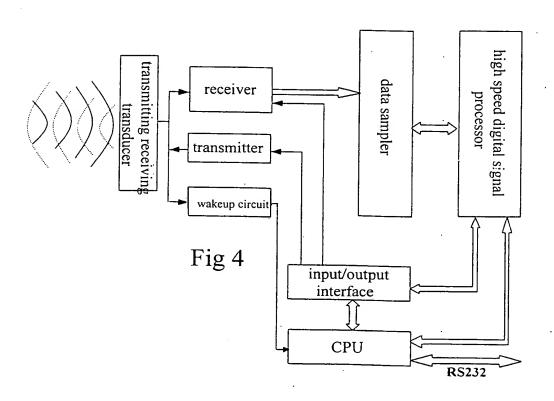
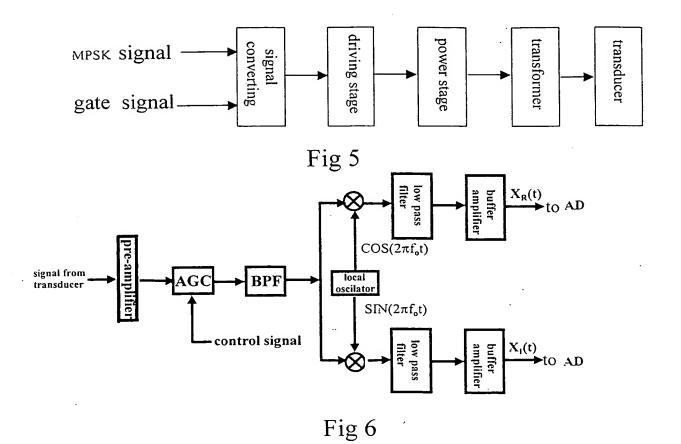
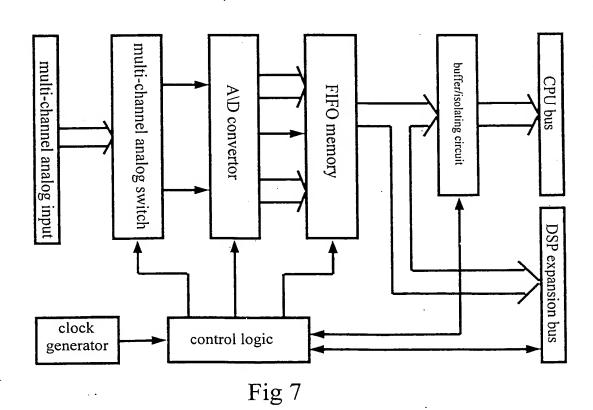


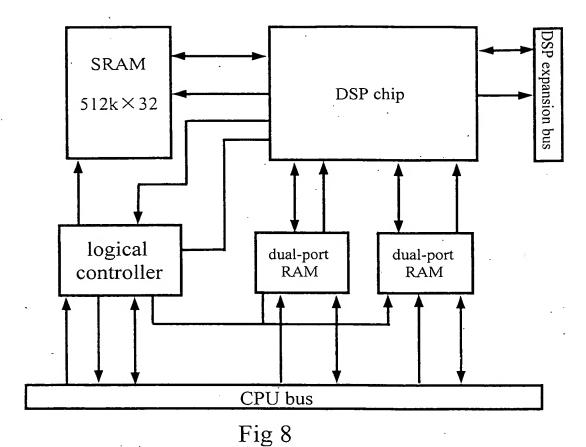
Fig 2











DA timer DA OC gate digital digital output port logical output controller 8 bits digital digital input port input main control computer bus

Fig 9

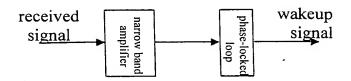


Fig 10

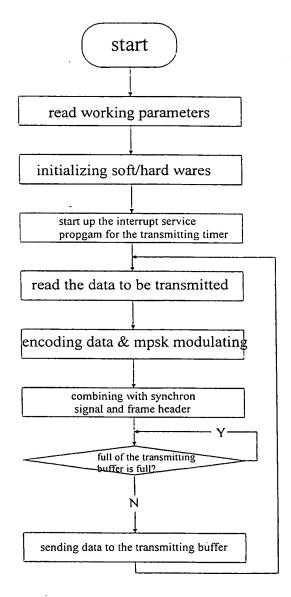


Fig 11a

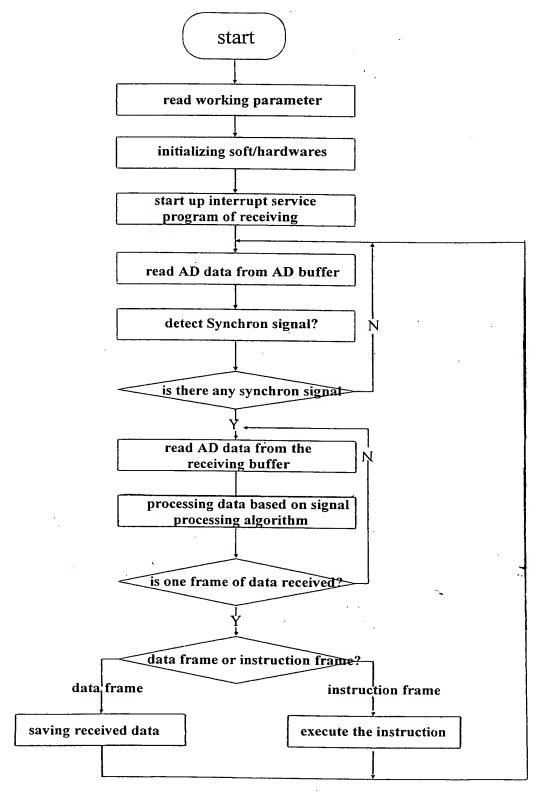


Fig 11b

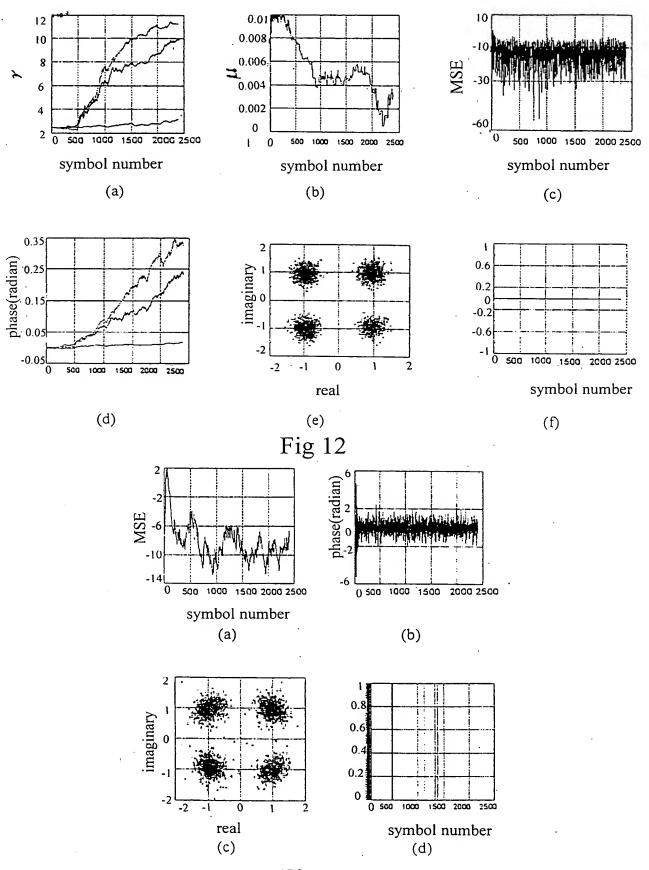


Fig 13

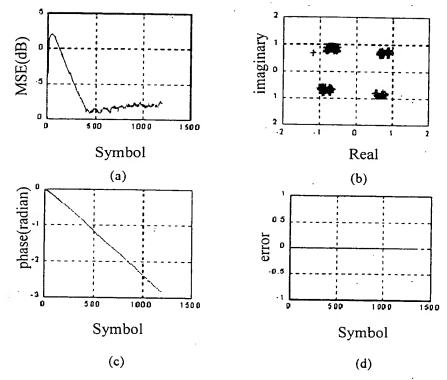


Fig 14



Fig 15

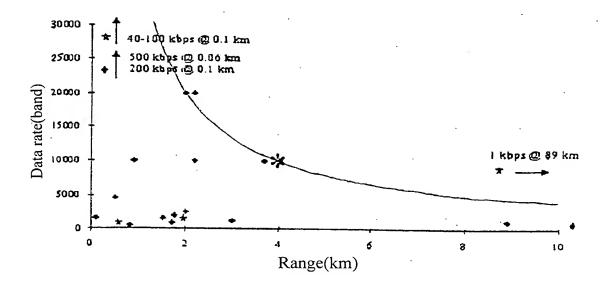


Fig 16

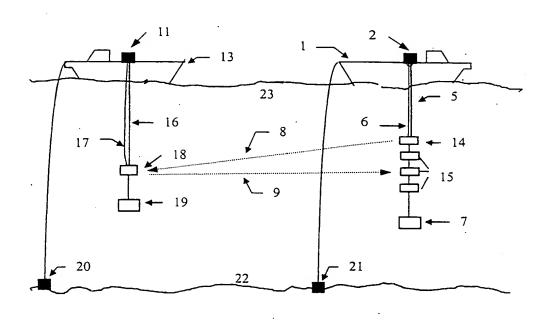


Fig 17

